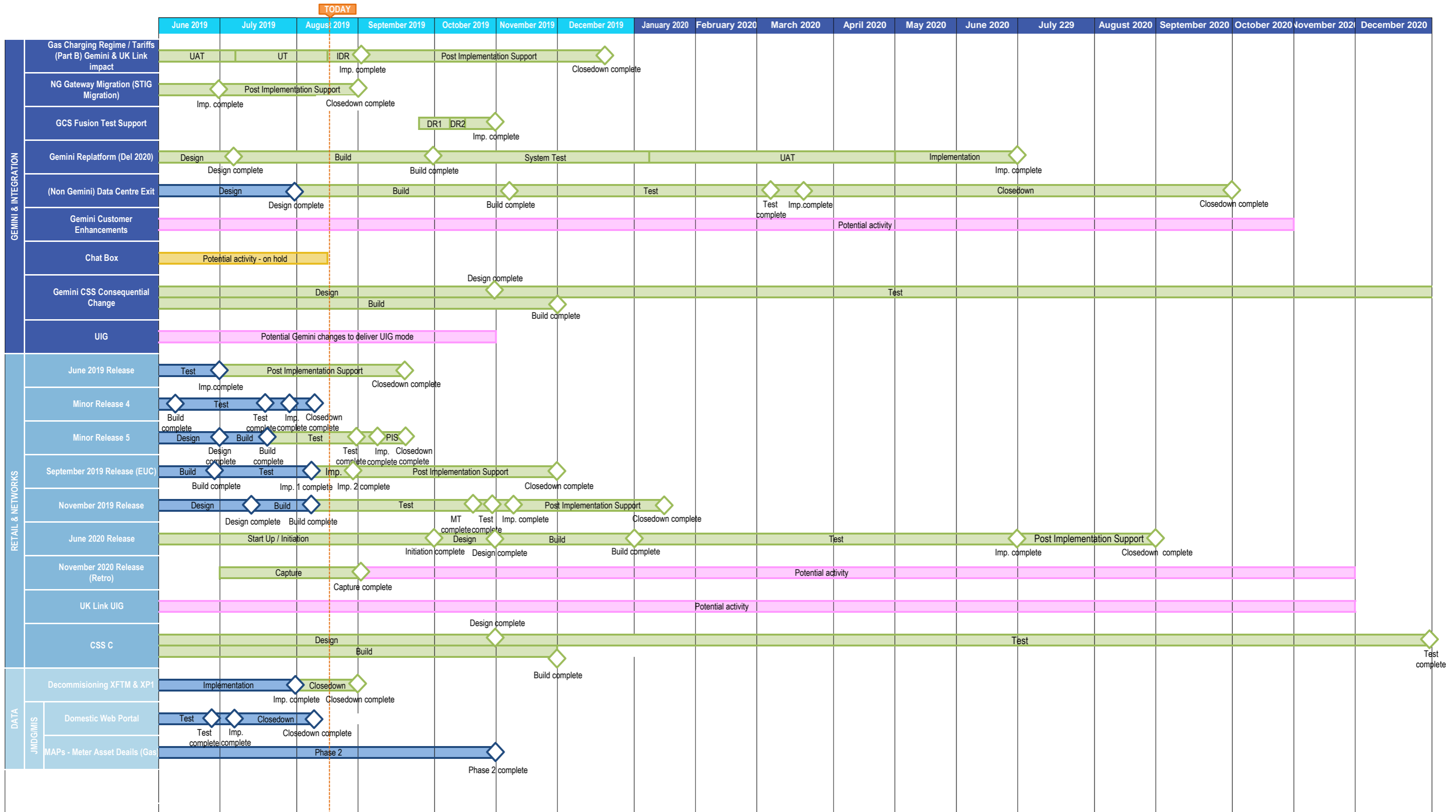


# PORTFOLIO ROADMAP - 14 AUGUST 2019



Key	Symbol	Description
◇	L1 milestone	L1 milestone
■	Stage / milestone complete	Stage / milestone complete
■	Stage / milestone planned	Stage / milestone planned
■	unplanned activity	unplanned activity
■	on hold pending further discussion	on hold pending further discussion
■	DBT - Design Build, Tes AT - Acceptance Test	DBT - Design Build, Tes AT - Acceptance Test
■	UT - Unit Test	UT - Unit Test
■	ST - System Test	ST - System Test
■	SIT - System Integration Test	SIT - System Integration Test
■	UAT - User Acceptance Test	UAT - User Acceptance Test
■	RT - Regression Test	RT - Regression Test
■	PT - Performance Test	PT - Performance Test
■	PEN - Penetration Test	PEN - Penetration Test
■	MT - Market Trials	MT - Market Trials
■	IDR - Implementation Dress Rehearsal	IDR - Implementation Dress Rehearsal
■	HLD - High Level Design	HLD - High Level Design
■	CUT - Code & Unit Test	CUT - Code & Unit Test
■	Imp. - Implementation	Imp. - Implementation
■	POC - Proof of Concept	POC - Proof of Concept